

10/04/121
01/11/02



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

NUM	FILED DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044121	01/11/2002	251	666	2316	Charles E. Law

INVENTORS: Riley John, Wagner Michael, Fickel Joseph

CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 5C/281,059 01/11/2001

FOREIGN APPLICATIONS VERIFIED:

PUB DO NOT PUBLISH		RESCIND	ATTORNEY DOCKET NO
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		TESSERA 3.0-299
35 USC 110 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		
Verified and Acknowledged by Examiner's Initials			
Stacked microelectronic assemblies and methods of making same.			
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)			

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
3/25/4		Assistant Examiner	
ISSUE FEE		Total 2	
Amount Due	Date Paid	Print Claim for O.G.	
\$ 1330.00		1	
TERMINAL DISCLAIMER		Jasmine Clark	
		Primary Examiner	
		PREPARED FOR ISSUE	
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